

GOVERNMENT DEGREE COLLEGE, MUMMIDIVARAM

I B.Sc. (Computer Science Honours) Time Table,
Academic Year 2024-2025 Semester II

0 (08.00 AM - 08:50 AM)	1 (08.50 AM - 09.40 AM)	2 (09.40 AM - 10.30 AM)	3 (10.30 AM - 11.20 AM)	4 (11.20 AM - 12.10 PM)	5 (12.10 PM - 01.00 PM)
MONDAY					
			Digital Logic Design (A.A.K) Room - 1	Digital Literacy (A.A.K) Room - 2	
TUESDAY					
				Digital Literacy (A.A.K) Room - 2	
WEDNESDAY					
THURSDAY					
	Digital Logic Design (A.A.K) Room - 1				
FRIDAY					
				Digital Logic Design Practicals (A.A.K) Room - 1	
SATURDAY					
		Digital Logic Design (A.A.K) Room - 1			

Name of the Faculty: A. Arvind Kalyan

Workload: -

1. Digital Logic Design Theory - 3 Hours
Practical - 2 Hours
2. Digital Literacy : 2 Hours